

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robbe et al.

Serial No.: 10/556,647

Filed: November 10, 2005

Title: Voltage Shift Control Circuit for
PLL

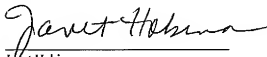
Group Art Unit: 2816

Examiner: Khareem E. Almo

Certificate of EFS-Web Transmission

I hereby certify that this paper is being
submitted to the Commissioner for Patents,
via EFS-Web, on this date:

January 14, 2010


Janet Habina

AMENDMENT C

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 223130-1490

Sir:

In response to the communication mailed October 14, 2009, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.